

CLAIMS

Please cancel claims 68-93 without prejudice or disclaimer and please add new claims as shown in the following claim listing.

1-93. (Canceled).

94. (New) A non-transitory, computer-readable medium having operating system software stored thereon, the operating system software, when executed by a processor, to:

 identify an entry of the processor into a C power state in accordance with Advanced Configuration and Power Interface (ACPI) Specification, Revision 2;

 identify a time corresponding to the entry of the processor into the C power state;

 read a time corresponding to an exit of the processor from the C power state, wherein the processor is to exit from the C power state in response to an interrupt and wherein the time corresponding to the exit is to be identified prior to execution of an interrupt routine by the processor in response to the interrupt; and

 determine a duration corresponding to the C power state based on the time corresponding to the entry and the time corresponding to the exit.

95. (New) The computer-readable medium of claim 94, wherein the C power state is a C1 power state.

96. (New) The computer-readable medium of claim 94, wherein the C power state is to be entered in response to a halt instruction.

97. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to cause the time corresponding to the exit to be identified prior to execution of the interrupt routine.

98. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the entry in a main memory.

99. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the entry in the processor.

100. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the entry in a chip.

101. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the entry in a chipset.

102. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the exit in a main memory.

103. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the exit in the processor.

104. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the exit in a chip.

105. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to store the time corresponding to the exit in a chipset.

106. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to cause a counter to be started to identify the time corresponding to the entry.

107. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to read a counter to read the time corresponding to the exit.

108. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to read a counter in a chip to read the time corresponding to the exit.

109. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to read a counter in a chipset to read the time corresponding to the exit.

110. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to cause a counter to be halted to identify the time corresponding to the exit.

111. (New) The computer-readable medium of claim 94, the operating system software, when executed by the processor, to select a C power state for the processor based on the determined duration and to cause the processor to enter the selected C power state.

112. (New) A non-transitory, computer-readable medium having operating system software stored thereon, the operating system software, when executed by a processor, to:

identify an entry of the processor into a C power state in accordance with Advanced Configuration and Power Interface (ACPI) Specification, Revision 2;

cause a counter to be started;

identify an exit of the processor from the C power state in response to an interrupt;

cause the counter to be halted; and

determine a duration corresponding to the C power state based on a content of the counter.

113. (New) The computer-readable medium of claim 112, wherein the C power state is a C1 power state.

114. (New) The computer-readable medium of claim 112, wherein the C power state is to be entered in response to a halt instruction.

115. (New) The computer-readable medium of claim 112, the operating system software, when executed by the processor, to cause the counter to be halted prior to execution of an interrupt routine by the processor in response to the interrupt.

116. (New) The computer-readable medium of claim 112, wherein the counter is in a chip.

117. (New) The computer-readable medium of claim 112, wherein the counter is in a chipset.

118. (New) The computer-readable medium of claim 112, the operating system software, when executed by the processor, to select a C power state for the processor based on the determined duration and to cause the processor to enter the selected C power state.